

# Design Guidelines

**For Manufacturing of Circuit Boards and  
Modules and for Layout Design**

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# 1 Intro

This document contains the design guidelines respectively the requirements for manufacturing high-quality printed circuit boards at Kontron Austria GmbH. If any of the outlined specifications/recommendations can't be followed, the detailed technical conditions have to be discussed.

## 1.1 Overview

### Circuit Board

This section describes preconditions for production-oriented circuit boards like dimensions, properties, fiducials, surface, panel design etc.

### Surface Mount Technology

In this section some design rules for designing SMT layouts (pad geometries, component gaps etc.) have been summarized. Other topics are the optimal data format and the production-oriented component packing.

### Through Hole Technology

This section describes the proper positioning of THT components in order to achieve an optimal wave soldering result. Further some recommendations regarding component gaps for mixed assemblies are given.

## 2 PCB / Layout

Kontron Austria GmbH expects acceptance criteria for circuit boards according to IPC-A-600.

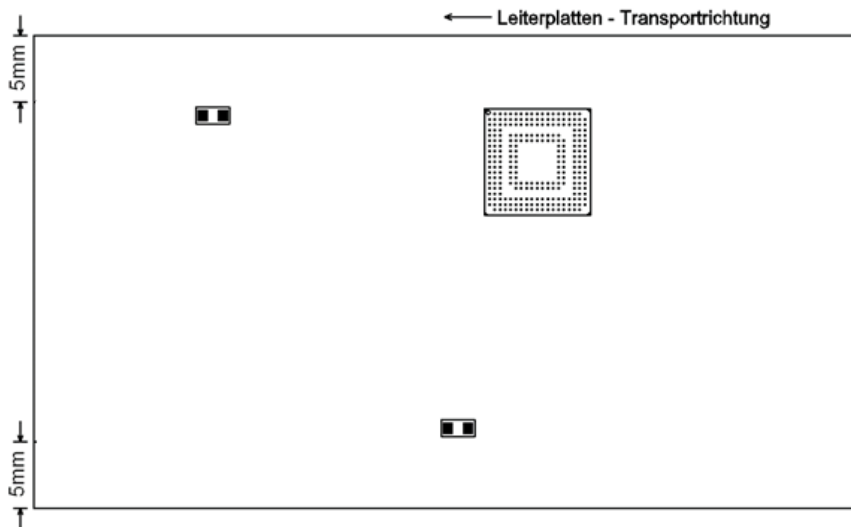
### 2.1 Min/Max PCB and Panel Dimensions

SMT Production Line		LP width	LP length	Thickness	Part-Size	Headroom
Linie1 / Linie2	Min.	50 mm	50 mm	0,4 mm	01005	--
	Max.	460 mm	450 mm	4,0 mm	--	--

Assembling automation		LP width	LP length	Thickness	Part-Size	Headroom
Siplace (SX2)	Min.	50 mm	50 mm	0,3 mm	01005	--
	Max.	560 mm	450 mm	4,5 mm	78mm x 78mm	--
Siplace (X4)	Min.	50 mm	50 mm	0,3 mm	01005	--
	Max.	535 mm	450 mm	4,5 mm	16mm x 16mm	--
AOI		LP width	LP length	Thickness	Part-Size	Headroom
Omron VT-S730	Min.	50 mm	50 mm	0,4 mm	01005	--
	Max.	460 mm	510 mm	4,0 mm	--	40 mm
Screen printer		LP width	LP length	Thickness	Pads <-> Outline	Headroom
DEK Horizon 265	Min.	50 mm	40 mm	0,4 mm	0,3 mm	--
	Max.	510 mm	508 mm	6,0 mm	--	42mm (BOT)
Reflow soldering		LP width	LP length	Thickness		Headroom
Seho Reflow (MaxiReflow 3.0)	Min.	50 mm	50 mm	--	--	--
	Max.	500 mm	650 mm	--	--	35 mm
Transportation bands		LP width	LP length	Thickness		
Asys transportation bands	Min.	50 mm	50 mm	--	--	--
	Max.	460 mm	460 mm	--	--	--
Wave soldering		LP width	LP length	Thickness		Headroom
Seho MWS - 2340	Min.	--	--	--	--	--
	Max.	460 mm	370 mm	--	--	10 mm (BOT)
Selective soldering		LP width	LP length	Thickness		Headroom
Seho PowerSelective	Min.	--	--	--	--	--
	Max.	350 mm	350 mm	--	--	70 mm (TOP) 30mm (BOT) or 14 mm (25 mm around solderpin)
Depaneling		LP width	LP length	Thickness		
CAB Maestro 4M	Min.	--	--	1,0 mm	--	--
	Max.	450 mm	350 mm	3,2 mm	--	--
Hölzer LOW4233 RD XL (Fräse)	Min.	--	--	--	--	--
	Max.	415 mm	475 mm	--	BOT: 40 mm TOP: 13 mm	--
Cleaning system		LP width	LP length	Thickness		
Kolb PSB500	Min.	50 mm	100 mm	--	--	--
	Max.	180 mm	460 mm	--	--	--
Potting machine		LP width	LP length	Thickness	Dosing amount	Needle <-> Components
Scheugenpflug LeanCNCCell 1000x1000 und A310	Min.	--	--	--	0,1 ml und 0,05ml/s	Z>30mm → 12mm Z<30mm → 5mm
	Max.	250 mm	480 mm	180 mm	30ml/Hub & 5ml/s	--

## 2.2 Clearance for transport bands

For transporting and clamping of the PCB, the SMT components shall have a distance to the edge of at least 5 mm. If the required 5 mm cannot be kept, then consultation with the NPI or CAD department of Kontron Austria GmbH is required. Maybe a special panel design or an additional edge has to be provided.



## 2.3 Base Material

FR4 (Standard at Kontron Austria GmbH)

If other base materials should be used / are required, please consult the CAD department of Kontron Austria GmbH.

## 2.4 Pad Surface

With respect to cost effectiveness and customer preference, the following procedure are preferred to obtain a solderable surface.

- Immersion tin (Sn) (on customer preference)
- Immersion silver (Ag) (on customer preference)
- Immersion nickel-gold (Ni-Au) **(Standard at Kontron Austria GmbH)**
- Hot Air Leveling (HAL) (lead-free) (after consultation)
- Organic Cu solderability preservative (OSP) (after consultation)

## 2.5 Layer Thickness of Circuit Board Surface

Layer thickness of circuit board surface has to ensure sufficient wetting during repeated soldering.

The following layer thicknesses are required:

- Immersion tin (Sn)  $\geq 1.0 \mu$
- Immersion silver (Ag)  $\geq 0,15u$
- Immersion nickel-gold (Ni-Au) 3u bis 5u / 0,05u bis 0,12u
- Hot Air Leveling (HAL) (lead-free)  $\geq 1u \leq 25u$
- Electrolytic gold typical 1,0u

## 2.6 Solder Resist Mask

Since the manufacturer of the circuit boards adapts the data himself (according to his process tolerances), solder resist data should have the same size as the copper pads.

Solder resist must also be provided between fine pitch pads.

To prevent short-circuits, vias should be covered with solder resist (only onesided) unless they are used as test points. If vias are placed directly in copper/solder pads (example: thermal Pads, ...) they have to be closed.

Different colors (on demand) can be realized - standard is green.

## 2.7 Silkscreen

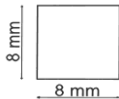
The print width and the gap to the pads is at least 0.2 mm. Pads must not be overprinted.

Different colors (on demand) can be realized - standard is white.

## 2.8 Clear Space for Barcode Labels

A clear space of 8 x 8 mm for our 2D barcode labels should be provided on the circuit board. If not possible, a 6 x 6 mm label is available too.

The distance/clearance between the barcode label and SMT parts should exceed 5mm.



## 2.9 Fiducials

Basically there are two different kinds of fiducials on the circuit board.

### 2.9.1 Circuit Board Fiducial

In order to have a reference point on the circuit board, at least two fiducials should be on the board, preferably in diagonal direction and with maximum distance. Fiducials should be placed on Top-Side and BOT-Side. (Even at onesided designs)

Preferably use different shapes for the two fiducials. This is especially important when providing symmetric designs.

**Minimum distance of fiducial to circuit board edge (x/y): 5mm / 5mm**

For boards in panels the panel should provide at least two additional fiducials. For positioning these marks the same guidelines as for the circuit boards should be applied.

### 2.9.2 X-out Fiducial

For PCBs in a panel there should be a separate x-out fiducial for every single board, placed on the edge of the panel, respectively close to the corresponding PCB.

In case of bad parts in the panel, the corresponding x-out fiducial will be pasted over by the PCB manufacturer.

Recommended fiducial geometry see 2.9.3

### 2.9.3 Recommended Fiducial

The surface of the mark should be smooth (even) and free of solder resist!

Following marks are preferred:

Circle (diameter 1.6 mm), circle (diameter 1.0mm), square, cross

## 2.10 Panel design

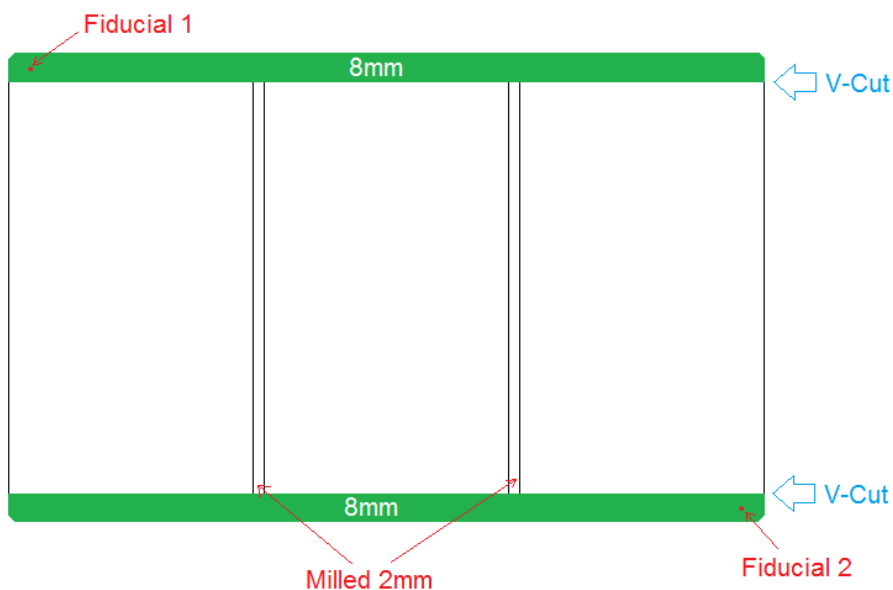
For de-paneling a minimum distance of 0,5 mm between the nearest circuit track / copper surface and outline of the circuit board shall be provided (also for inner layers - power planes). For bigger panel designs please consider some additional space (example: additional panel boarder in the middle) for the mechanical assistance system of the production line. Panel corners have to be chamfered.

For optimal panel design, please consult the NPI department of Kontron Austria GmbH.

### V-Cut panelization (preferred):

Keep a minimum distance of 2mm between assembled parts and outline (V-Cut).

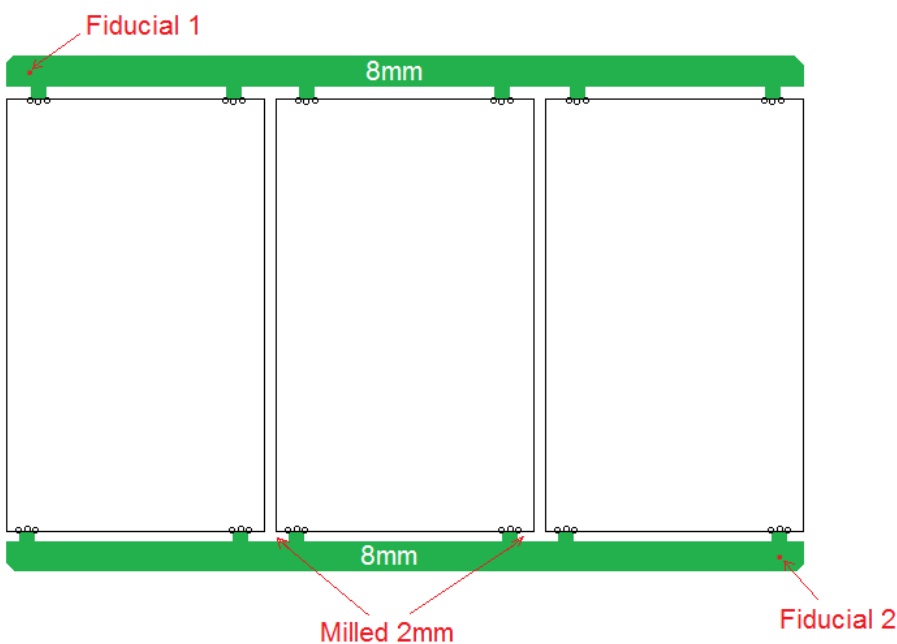
For FR4 PCBs suitable for a thickness between 1.0 and 3.2mm.



### Drilled panel tabs panelization:

If the outline is milled, the drills (panel tabs) should be placed into the circuit board

☑ no rework required



## 2.11 PCB data

The circuit board data should contain the following information:

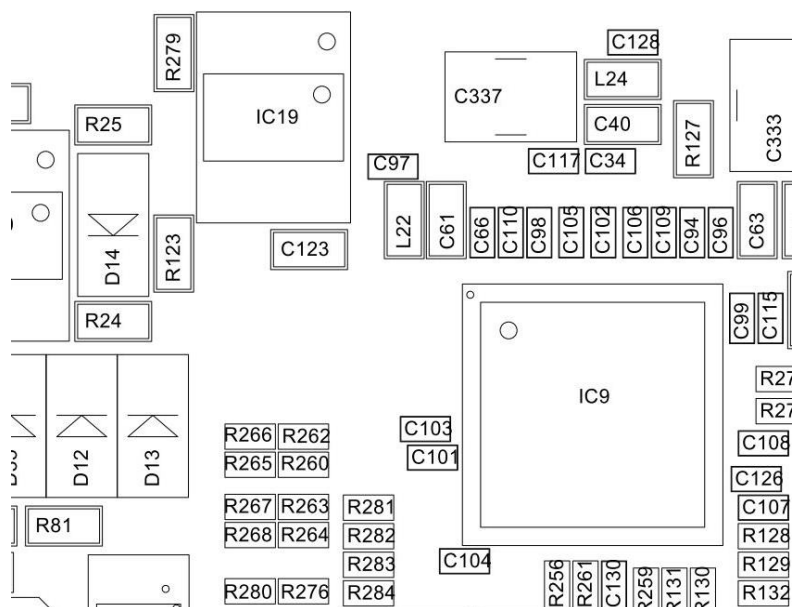
- Layer data in Extended Gerber Format
- Specification file with notes on:
  - Base material
  - Circuit board thickness
  - Copper thickness all layers (base copper / finished copper)
  - Surface
  - Colour of solder mask
  - Assembly print \*
  - Layer design / key to Gerber data names
  - Special remarks
- Drill data in Excellon format (seperate file for NPTH and PTH)
- Layer for Pastemask
- Design file for optical check and In-Circuit-Test (ODB++, Cadif, for Eagle Designs brd file or Fabmaster)

\* Assembly Print Notes:

The Assembly Print (pdf, etc...) has to contain:

- All placeable Parts (not assembled parts too)
- Part-Outlines
- Part-Names (R1, C1, ...)
- Direction of unipolar Parts

Example:



Please take care that every part name can be assigned to the part clearly.



## 3 SMT

### 3.1 Component packaging

- Strip (Tape&Reel) (preferred)
- Tray (preferred)
- Tube (only for small batches)

The components should be capable for automated assembling. If special shapes or plugs are used, the components should be available with pick & place pads. If not, please contact the NPI department at Kontron Austria GmbH; maybe tests can be made or special tools can be designed.

### 3.2 Pad Geometry

Basically, for each component there's a land pattern which represents the manufacturer's recommendation for the pad geometry – see datasheet.

Optimal pad geometries are described in detail in the IPC standard. To achieve the optimal geometry, a Land Pattern Generator can be used:

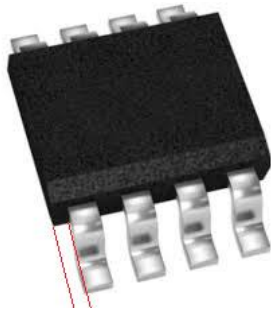
<http://www.pcblibraries.com/>

### 3.3 Minimum Distance between parts

Min. distance pad (copper) <-> pad (copper): 0.2mm

- This distance is needed to apply solder resist between the pads.
- Min. distance is needed between pads with same el. potential too.

In case, the part body exceeds the part pins, a minimal distance (part body <-> part body) >0.1mm is needed.



Both distance-guidelines presuppose that the component heights are similar to each other. If the component-heights are very different and the distance between parts are very small, collisions between assembly-tool and the components may occur. Assembly sequence can be controlled by Kontron Austria GmbH, although please be aware of that topic.

### 3.4 Assembling Data (Pick & Place data)

Attention: coordinate data for SMT assembling have to contain following information.

- Assembly position of all placeable parts (not assembled parts too)
- Component identification / design
- X coordinates from center of component (unit = mm, other units please indicate)
- Y coordinates from center of component (unit = mm, other units please indicate)
- Rotation
- Side: TOP/BOT

The coordinate file should also contain the positions of the Fiducials.

The origin of assembly data (xy) should be the lower left edge of the PCB.

Data have to be separated by a distinct separator mark.

#### 3.4.1 Example

```
D5|0805LED_KPT2012MGC|38.418|29.210|270.0
D6|0805LED_KPT2012MGC|38.418|24.130|270.0
D7|0805LED_KPT2012SYC|59.055|66.358|90.0
F1|EIA481-2_SMD2920P075TS|43.180|26.670|270.0
IC1|TQFP44_T89C51CC01UA|35.560|56.515|0.0
IC2|SO08_82C251T|45.085|5.080|180.0
IC3|NB|31.750|26.670|0.0
FID1|Fiducial_1|10.000|5.000|0.0
FID2|Fiducial_2|150.000|95.000|0.0
```

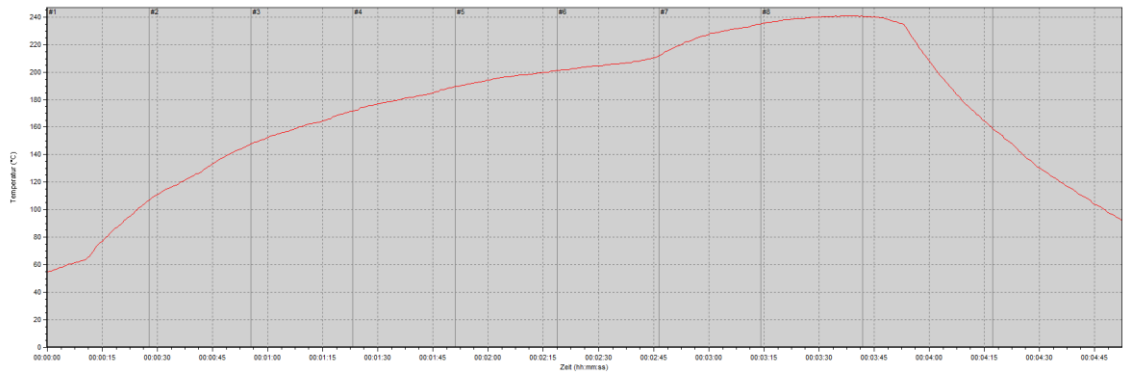
### 3.5 SMT Soldering

#### 3.5.1 Double-sided

In order to avoid that components detach from the circuit board during the second soldering process, following notes should be considered:

- The circuit board and the components should endure the additional temperature stress without defects.
- The components should be arranged on the circuit board such that all light-weight parts are on one side and all heavy parts on the other side. This is a precondition that no component detaches from the board during the second soldering process.

### 3.5.2 Soldering Profile Reflow



Example – Actual settings depend on the product

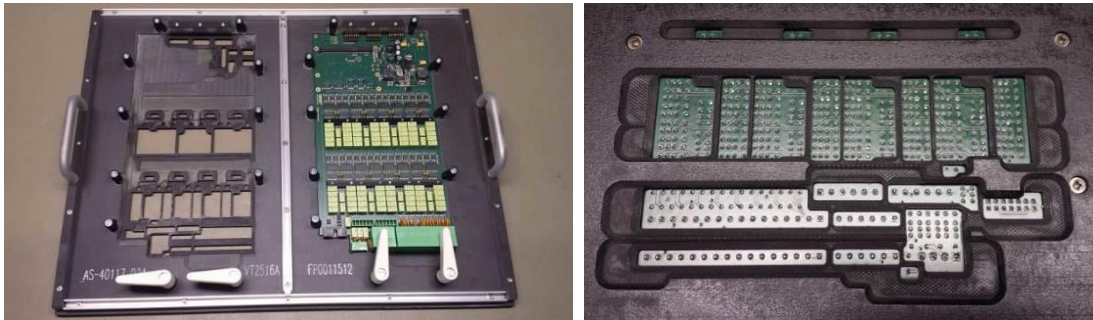
## 4 THT

### 4.1 THT Soldering

By means of optimized component arrangement the efficiency during manufacturing can be increased considerably.

#### 4.1.1 Selective Wave Soldering

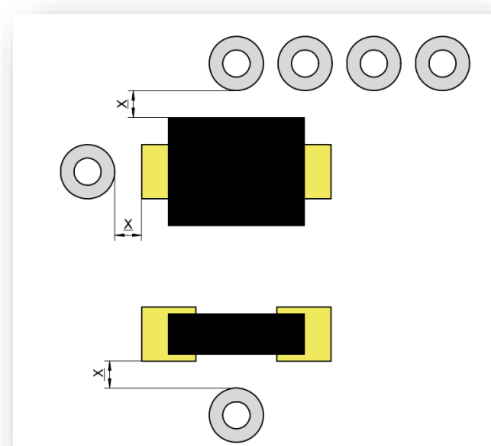
In case there are THT components on a double-side SMT circuit board, the layout has to permit machine wave soldering with a soldering mask.



Preconditions for the use of a soldering mask are:

- Clearance space between the THT pins to be soldered and SMT components of at least "x"mm.

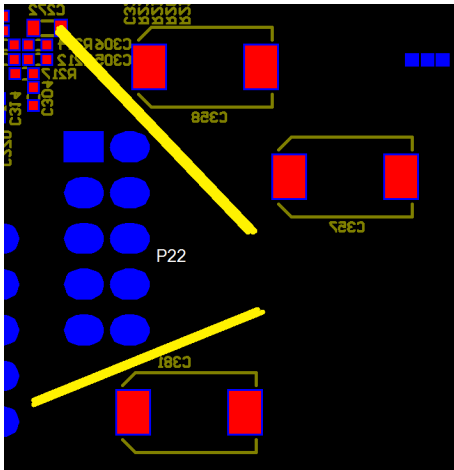
Component Height	Min. Distance „x“
<= 1,5mm	1,5mm
1,6mm bis 3,0mm	2,0mm
3,1mm bis 4,5mm	2,5mm
4,6mm bis 10,0mm	3,0mm



- In case of using titanium-material, the minimal distance "x" can be reduced by 30%. Of course, tooling costs increase.
- SMT component height on the soldering side max. 10mm

Examples:

Critical THT-part to be soldered: P22

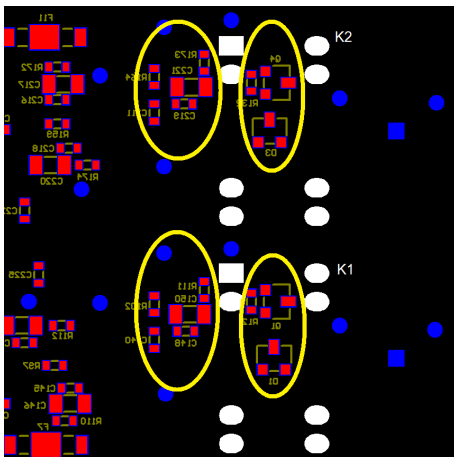


The distance between the SMT-parts C358/C381 and the THT-Pins (part P22) that have to be soldered is 2.1mm.

If C358/C381 would be chip size (0402, 0603, 0805, ...) the distance would be enough.

But the height of C358/C381 exceeds chip-size, so the distance to P22 has to be increased.

Critical THT-parts to be soldered: K1, K2



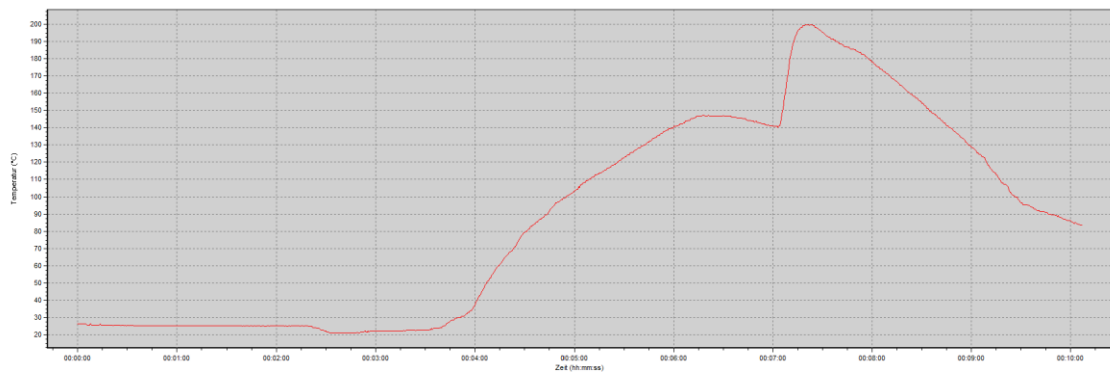
The distance between the SMT-parts and the THT Pins to be soldered is only 0.15mm.

Even if the SMT-parts would be chip-size, the distance would be too small.

To solder K1, K2 with selective wave soldering process, the SMT-parts have to be replaced by the CAD engineer.

White... THT-Pins (parts K1, K2) to be soldered  
Red... SMT-Pads/Parts  
Yellow... SMT-parts that have to be replaced

### 4.1.2 Soldering Profile Wave



Example – Actual settings depend on the product

### 4.1.3 Selective Soldering

For prototypes and small serial quantities, a THT selective soldering system is available. The design requirements correspond in principle to THT wave soldering with solder mask. However, components with the following height may be located on the solder side:

Assembly Side:

max. 70mm

Solder Side:

max. 14mm (if the distance to solder pin is smaller than 25mm) otherwise max. 30mm

For a detailed assessment of the machine solderability of components / designs, please consult NPI department at Kontron Austria GmbH.

## 5 Design for Testability

The design of the board has a significant impact on the achievable quality of testing.

### 5.1 Constructive Designrules

#### 5.1.1 Contacting

To permit an In-Circuit-Test, all electrical nets of the board have to be contacted. It is possible to contact on test pads (priority 1 – preferred), THT-pins (priority 2) and vias (priority 3).

Single-side contacting (only TOP, only BOT) keeps adapter cost low.

#### 5.1.2 Center Holes

To center the board in the test adapter, the design should contain additional (same drill-process than the other holes on the board) center holes. The diameter should be in the range of 2.00mm – 3.5mm. To avoid an wrong insertion of the board, the center holes should be placed asymmetrical.

#### 5.1.3 Test Points

##### Priority 1: 100mil Contact-Needle (default)

- Required diameter of test pads: >0.8mm
- Required distance from test pad to test pad (center <-> center): >2.05mm

##### Priority 2: 75mil Contact-Needle

- Required diameter of test pads: >0.4mm (preferred >0.5mm)
- Required distance from test pad to test pad (center <-> center): >1.65mm

##### Priority 3: 50mil Contact-Needle (please consult Kontron Austria GmbH)

- Required diameter of test pads: >0.2mm
- Required distance from test pad to test pad (center <-> center): >1.25mm

## 5.2 Electrical Designrules

### 5.2.1 Functional tests with high current loads

For currents above 0.5A, provide more than one contact points (test pads, THT-pins, vias)

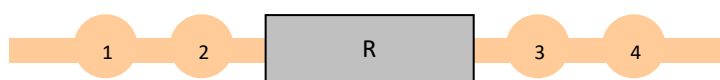
### 5.2.2 Arrays / Serial connections of resistors

To measure resistance-arrays make sure that at least 1 of the internal resistors is contactable.

To measure resistors that are connected in series it's sufficient to provide contact points at both ends of the serial connection.

### 5.2.3 4-wire measuring

To measure resistors smaller 22Ω, 4 contact points are required. (-> drawing). If not possible, at least 3 contact points have to be provided (1, 2, 3 or 2, 3, 4)



Drawing: 4-wire measuring

## 6 Design for Coatability

The layout has to be suitable for all common coating methods (dipping, flooding, spraying, etc.). Components that have not to be coated (plugs, sockets, switches, buzzer, dip switches, LEDs, etc.) must be maskable.

Requirement:

The clearance around components and areas of the PCB that are not allowed to be coated must exceed 2mm.

## 7 Design for Potting

### 7.1 Automated Potting

Please be aware of some kind of parts that may not be suitable for potting.

Examples:

- Plug connections, even if they are plugged in
- Transmitter/Receiver modules, antennas, ...
- Switches, Relais, ...

PCB, housing, frame must be leakproof.

Examples:

- Vias
- Mounting holes

For detailed informations about potting materials, potting process itself or all kinds of feasibility analyses please contact Kontron Austria GmbH.



## 8 References

Acceptance criteria for electronic assemblies - IPC-A-610

## 9 Contact Person

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